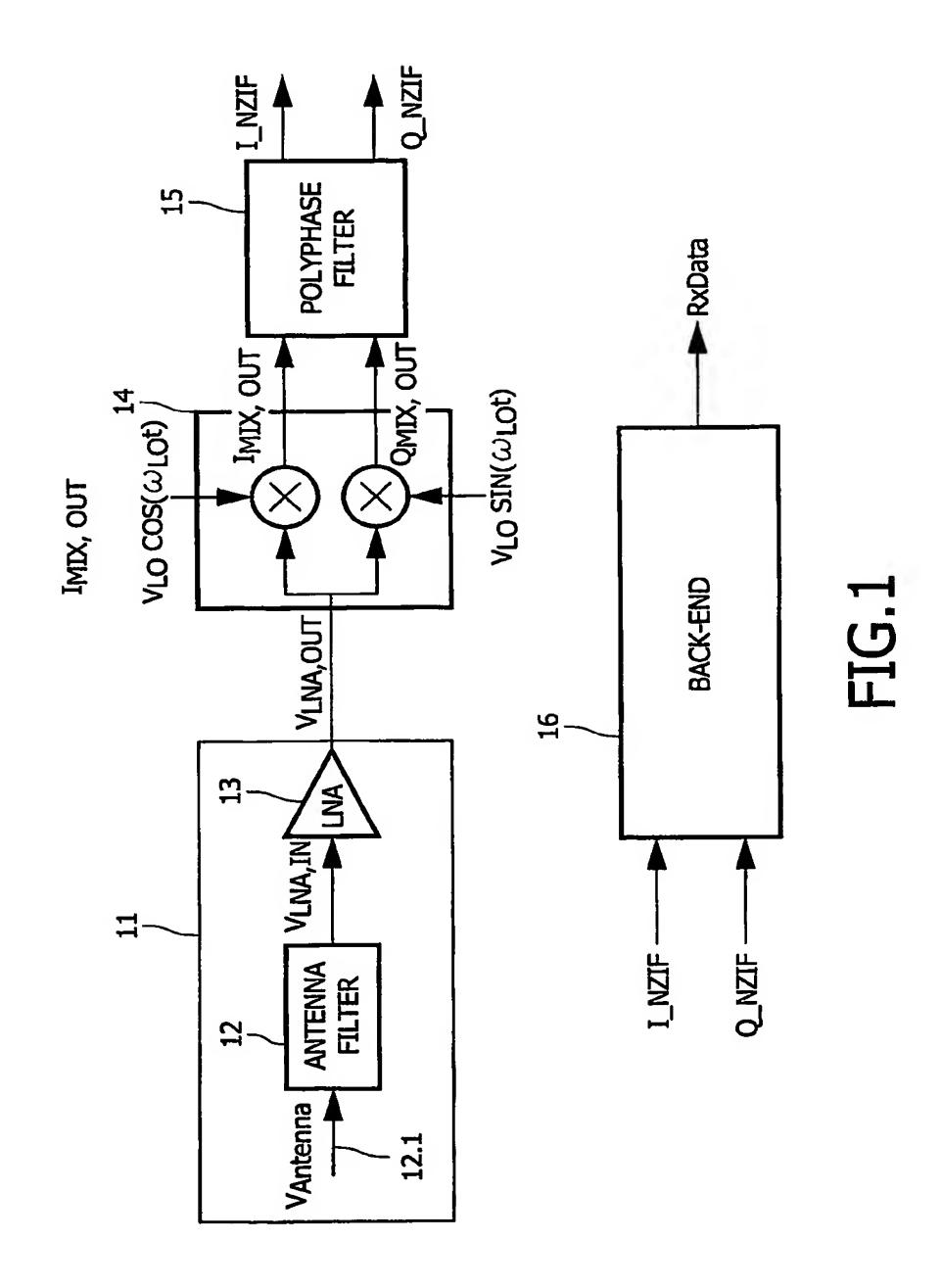
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- 13. The apparatus (20) of claim 4, wherein the average detector (37.1) is a sliding average detector that continuously generates a mean value of the frequency demodulated signal (demod\_lp2).
- 5 14. Receiver (10) comprising an apparatus according to one or more of the preceding claims.
  - 15. The receiver (10) of claim 14, comprising an analog front-end (10, 14, 15) and a digital back-end (16), said apparatus (20) for determining a frequency offset error being part of said digital back-end (16).
  - 16. The receiver (10) of claim 14 or 15 being designed to receive and process FSK or GFSK modulated antenna signals.

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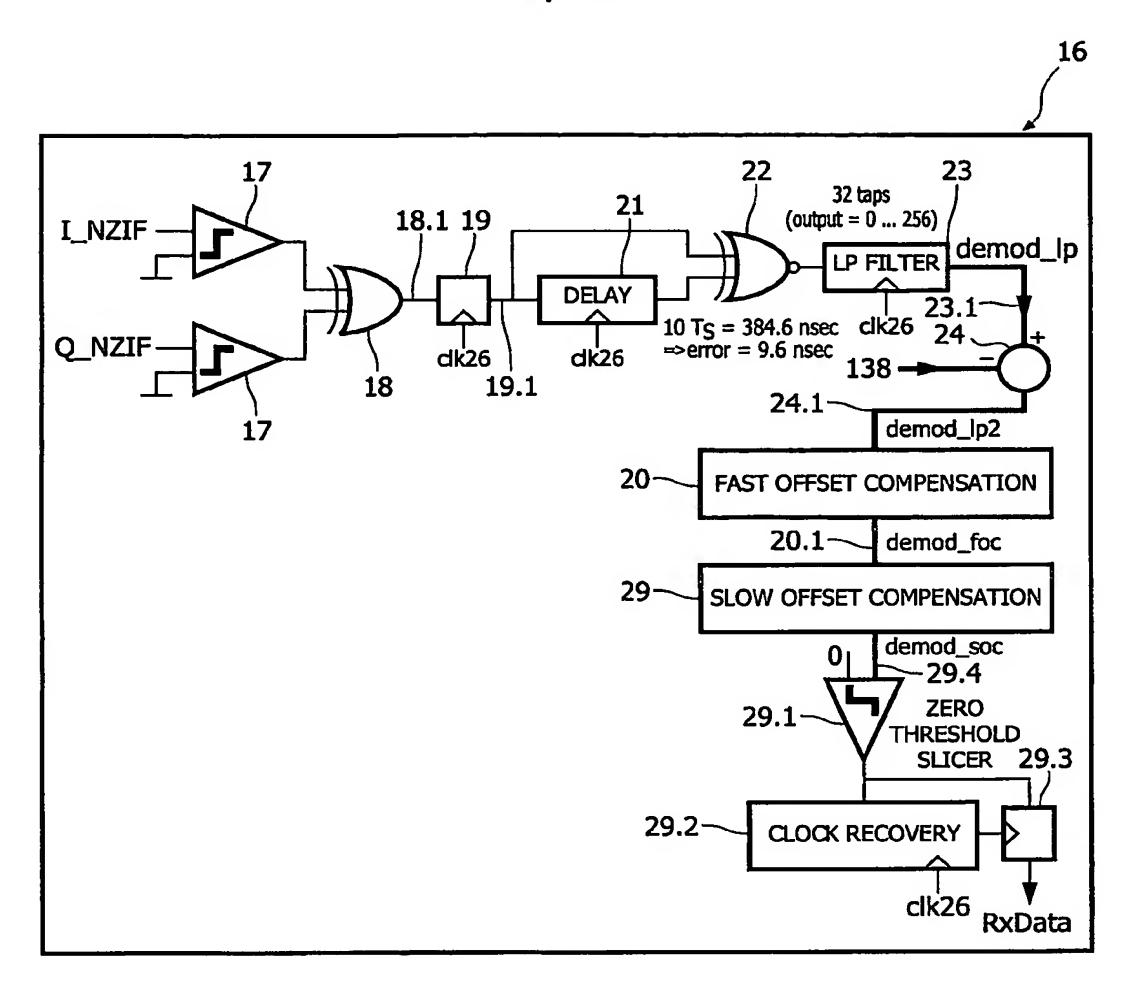


FIG.2

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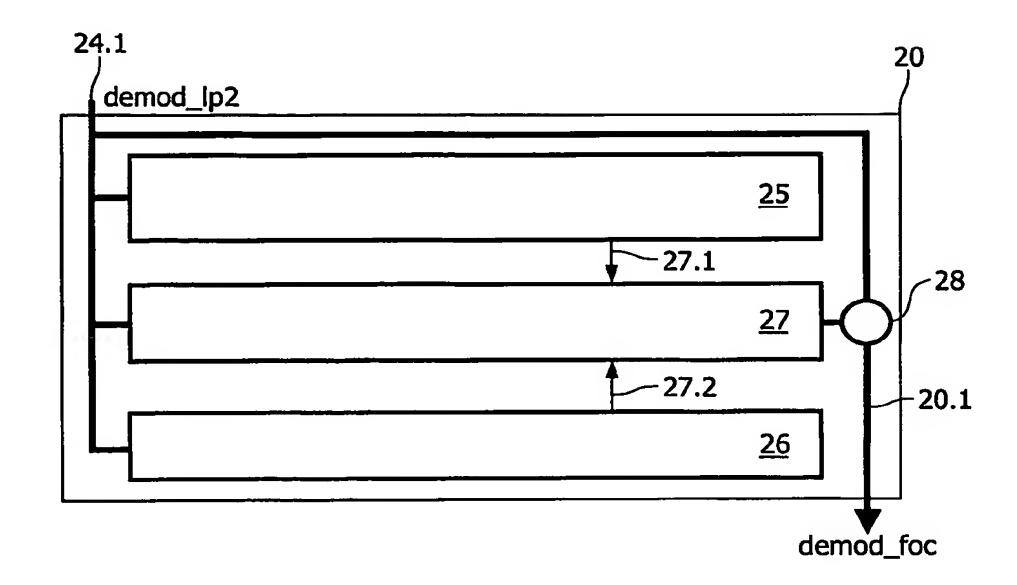


FIG.3A

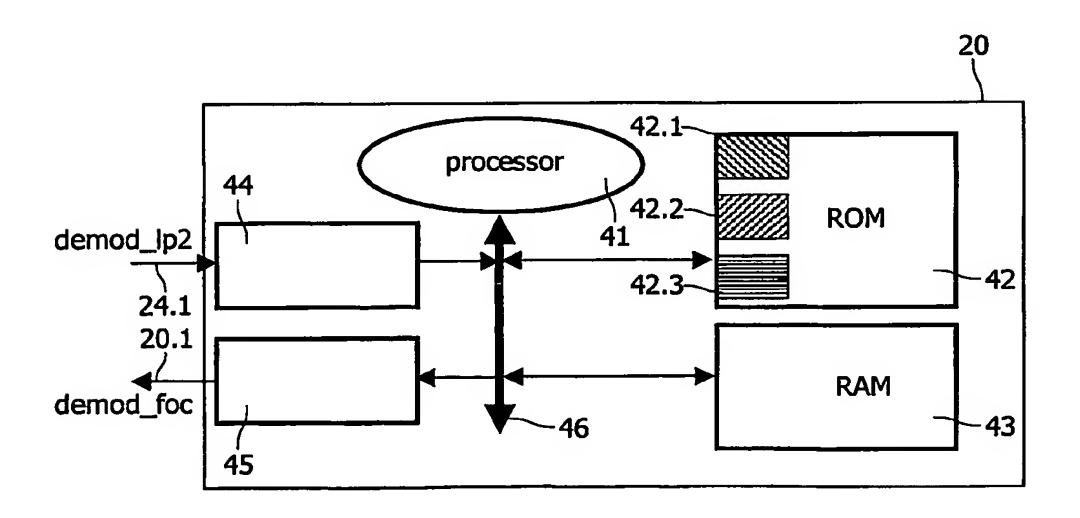


FIG.3B

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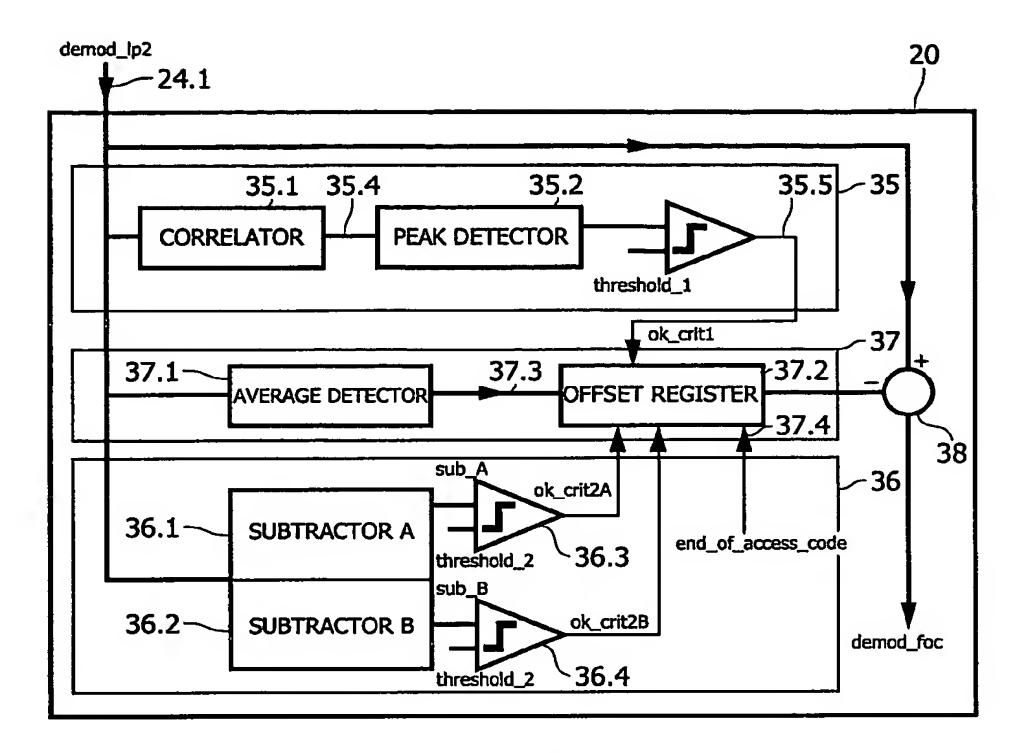


FIG.4

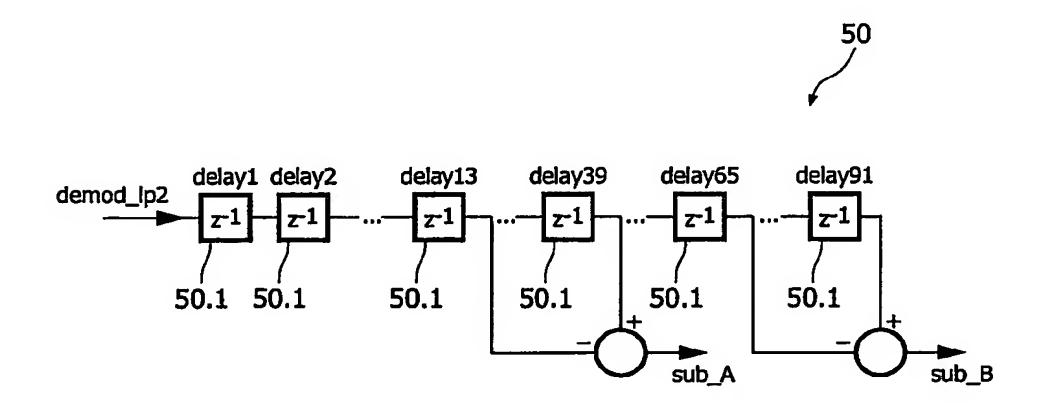
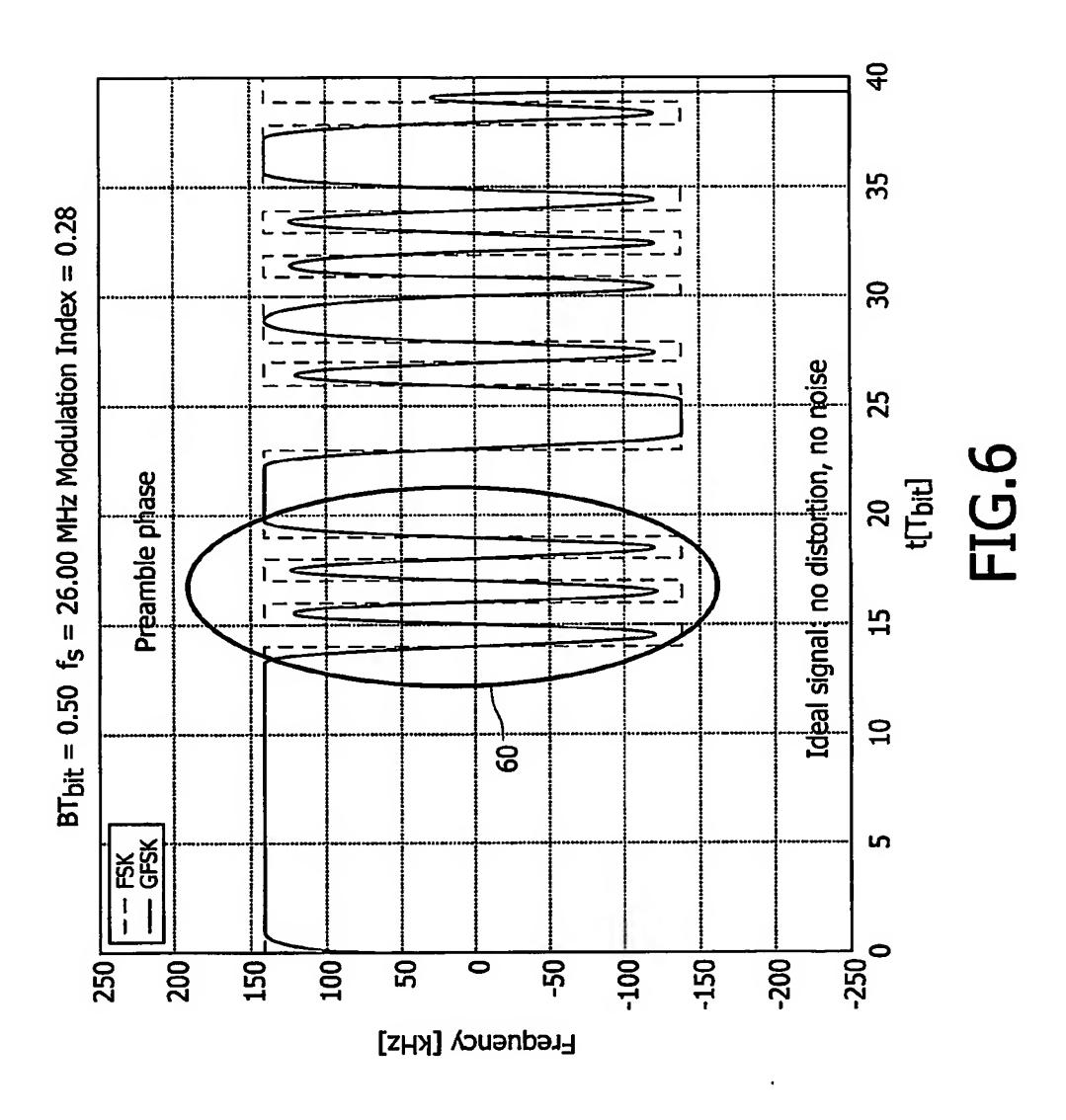
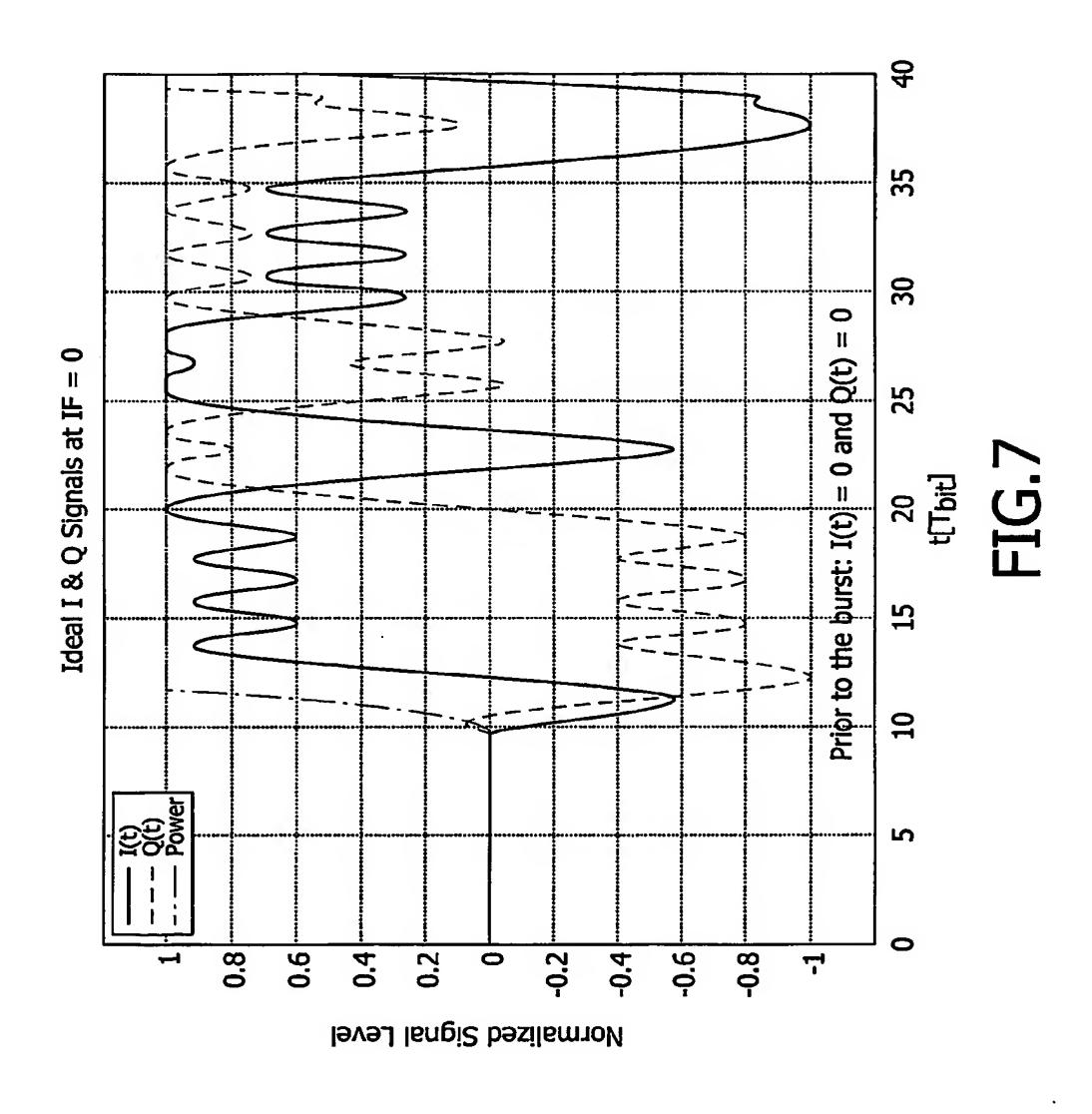
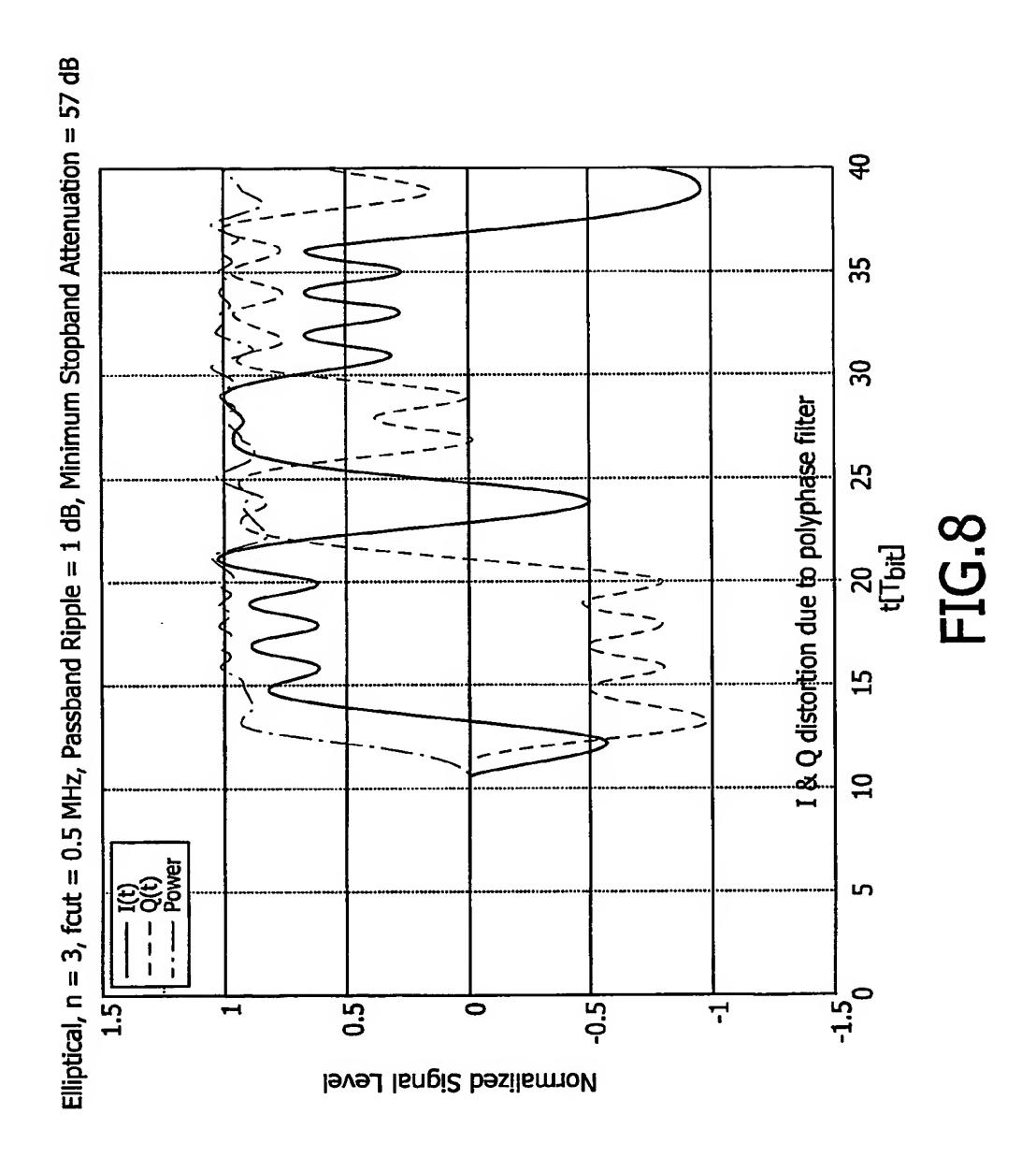
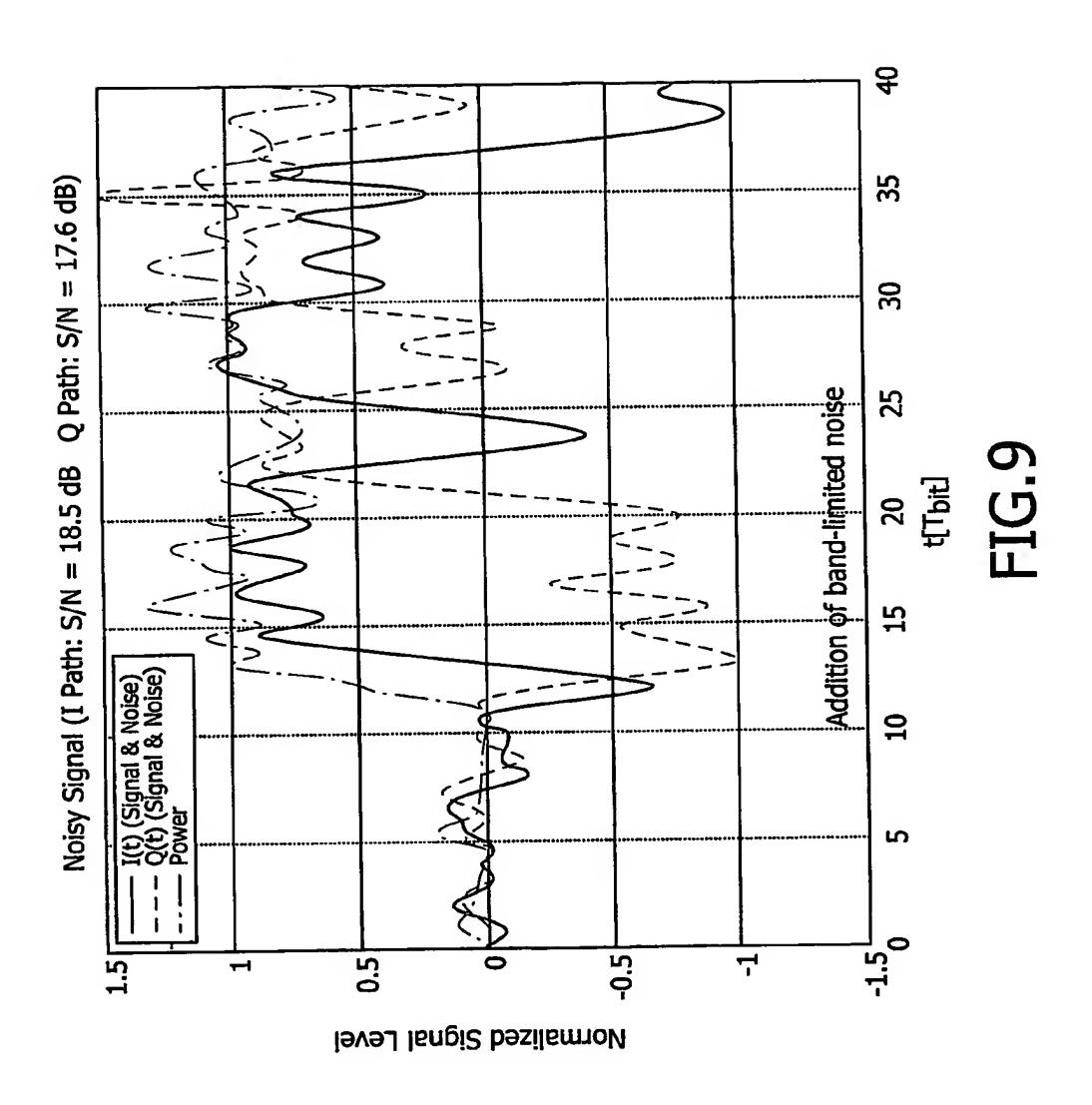


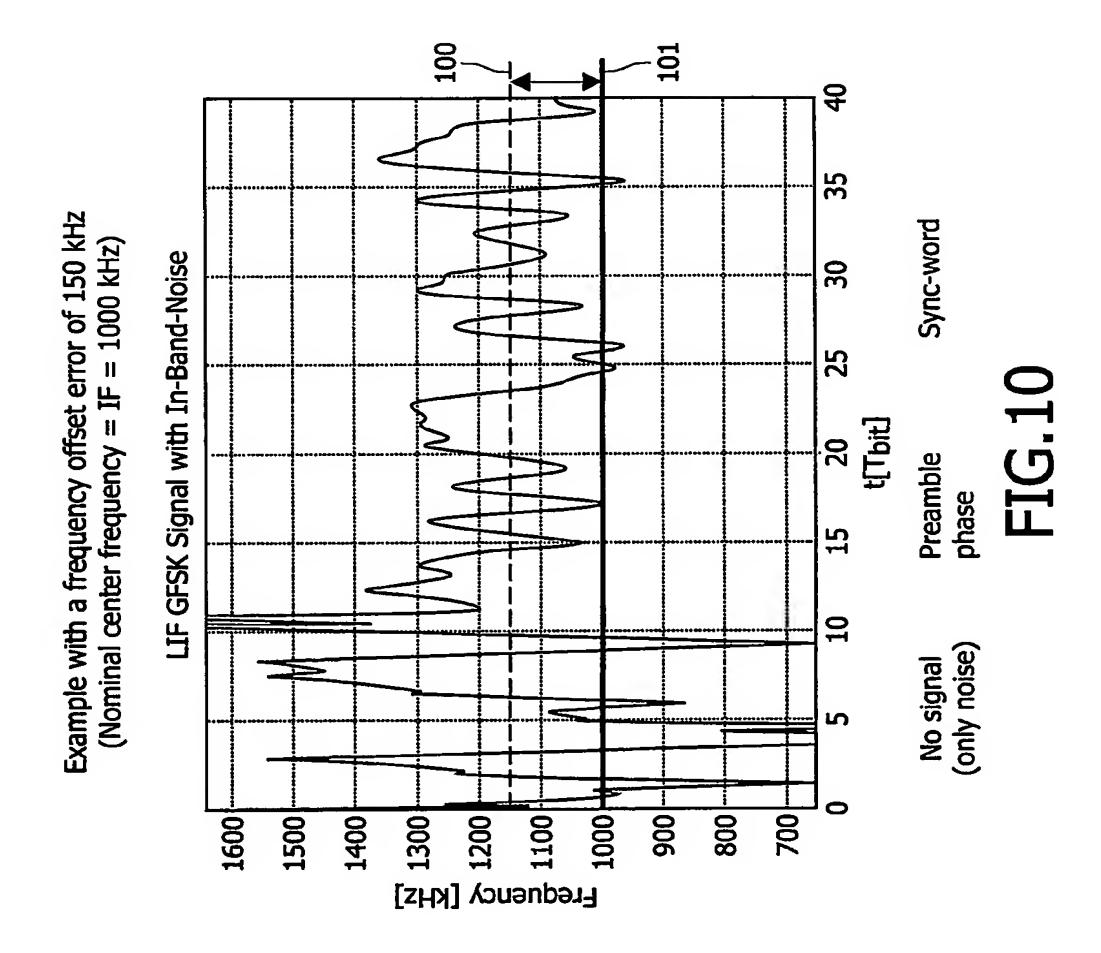
FIG.5



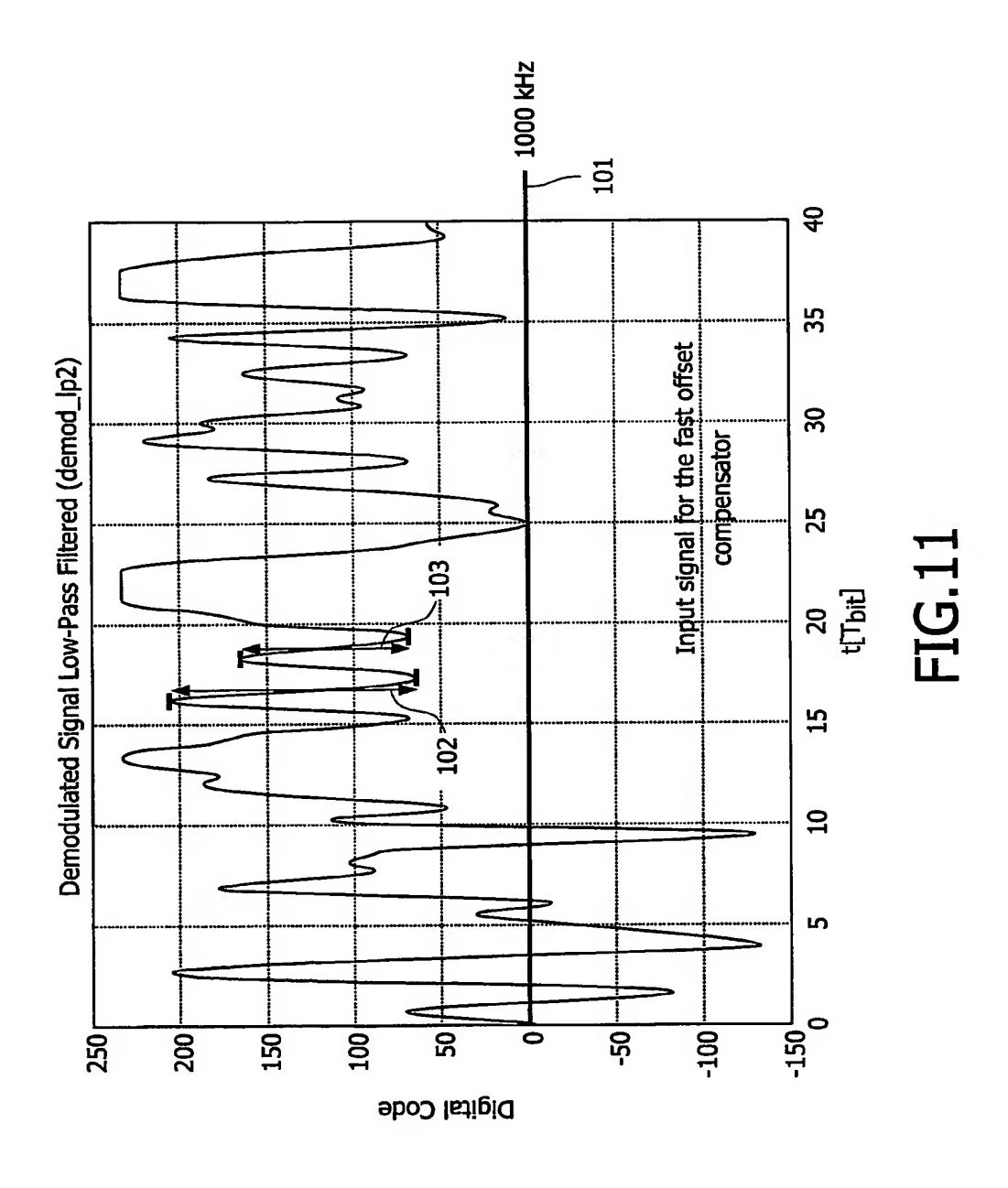




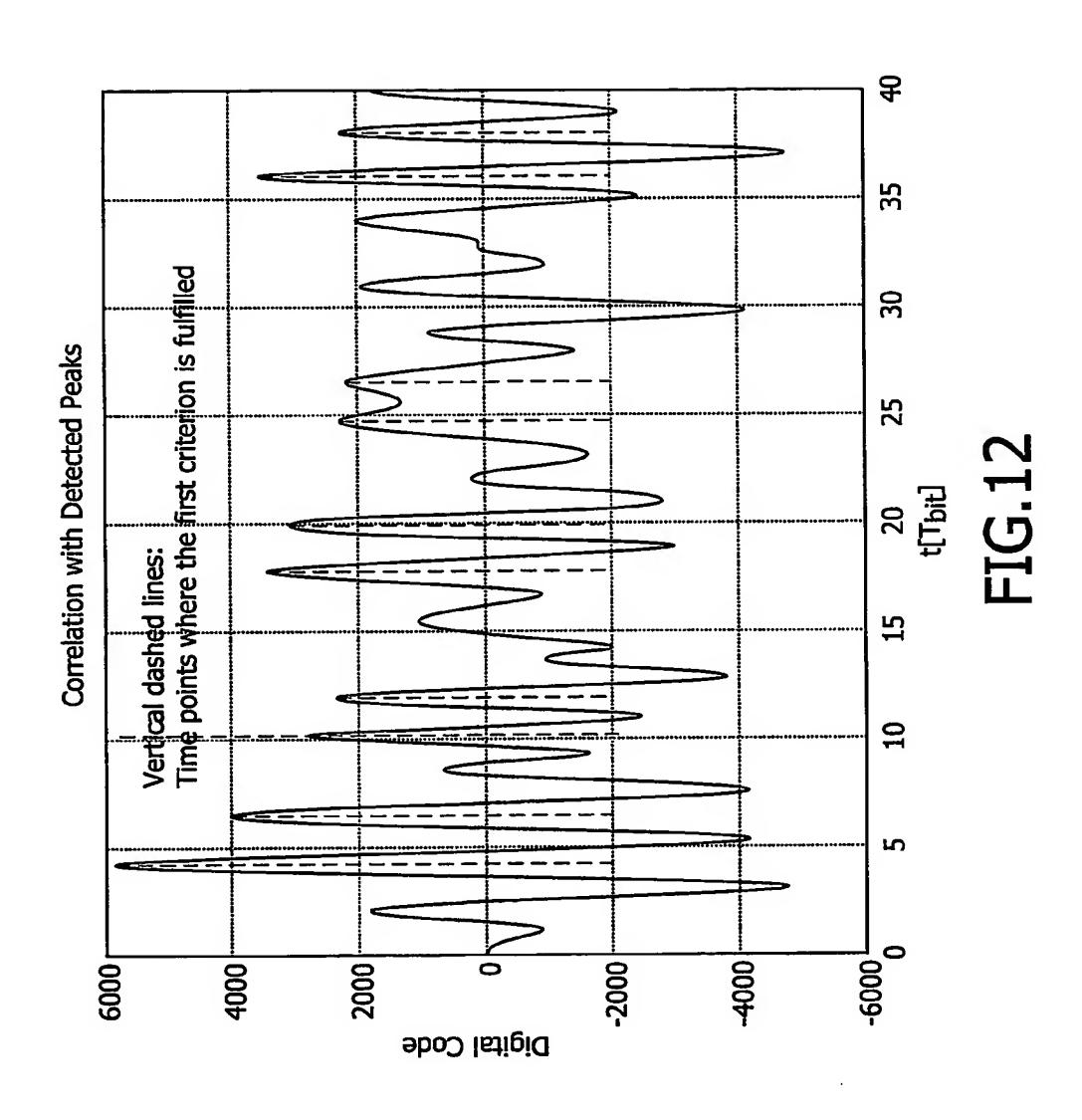


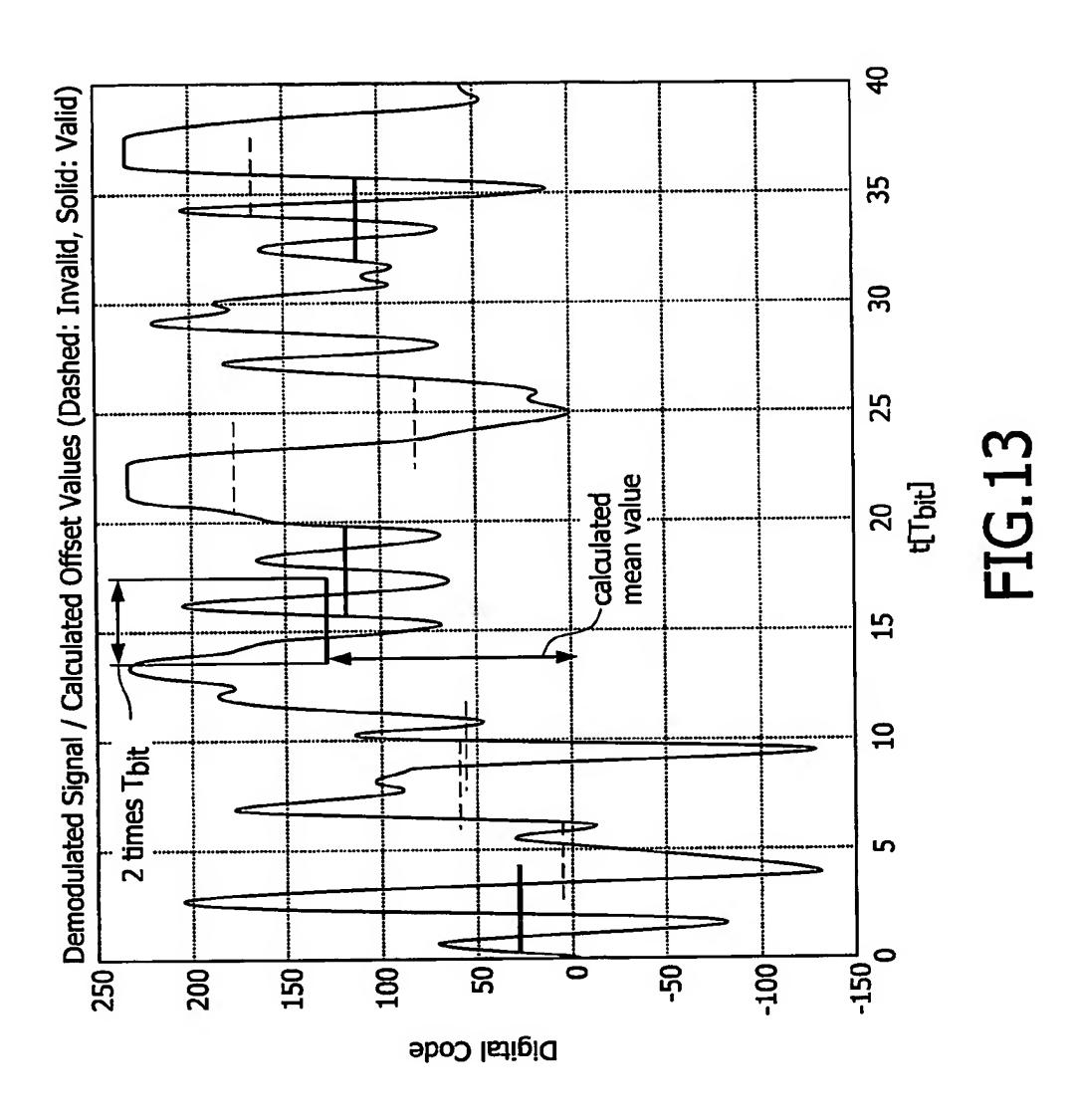


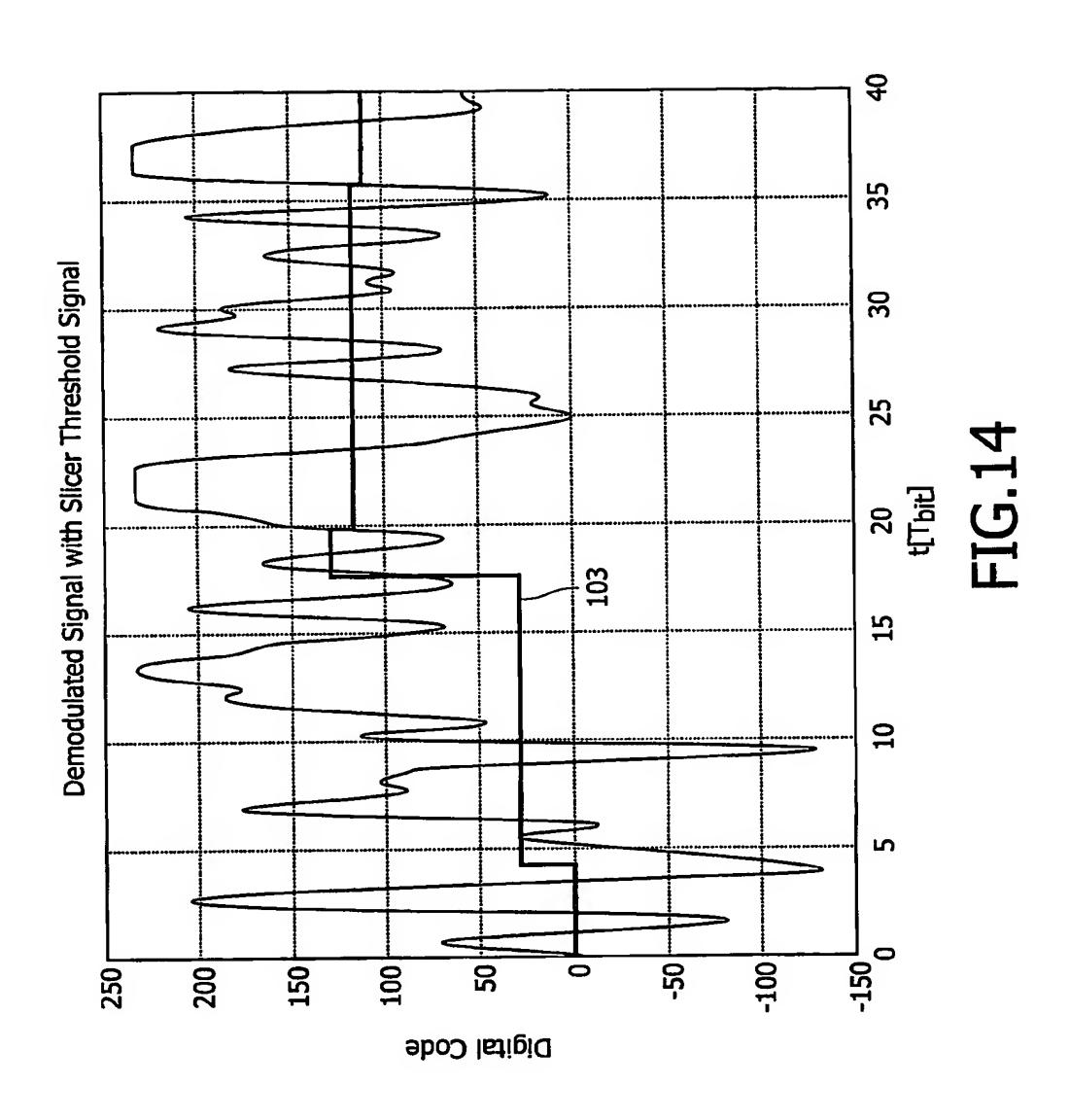
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#### INTERNATIONAL SEARCH REPORT

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IPC 7	H04L25/06 H04L27/14 H04L27/	152				
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Minimum do	ocumentation searched (classification system followed by classification	tion symbols)				
IPC 7	H04L					
Documentat	tion searched other than minimum documentation to the extent that	such documents are included in the fields s	earched			
Electronic d	ata base consulted during the international search (name of data b	ease and, where practical, search terms used	1)			
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	paragraph '0040! - paragraph '00 paragraph '0036!					
	paragraph '0002! - paragraph '00 abstract					
ļ						
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X Furti	her documents are listed in the continuation of box C	Patent family members are listed	in annex			
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1	3 January 2005	25/01/2005				
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